

```
graph LR; In(( )) -- 27 --> ADC[A/D CONVERTER]; ADC -- 33 --> C[CONTROLLER]; C -- 35 --> ADC; C -- 34 --> Comp[COMPUTATION]; Comp --> DI[DISPLAY I/F]; DI -- 36 --> C; subgraph 30 [ ]
```



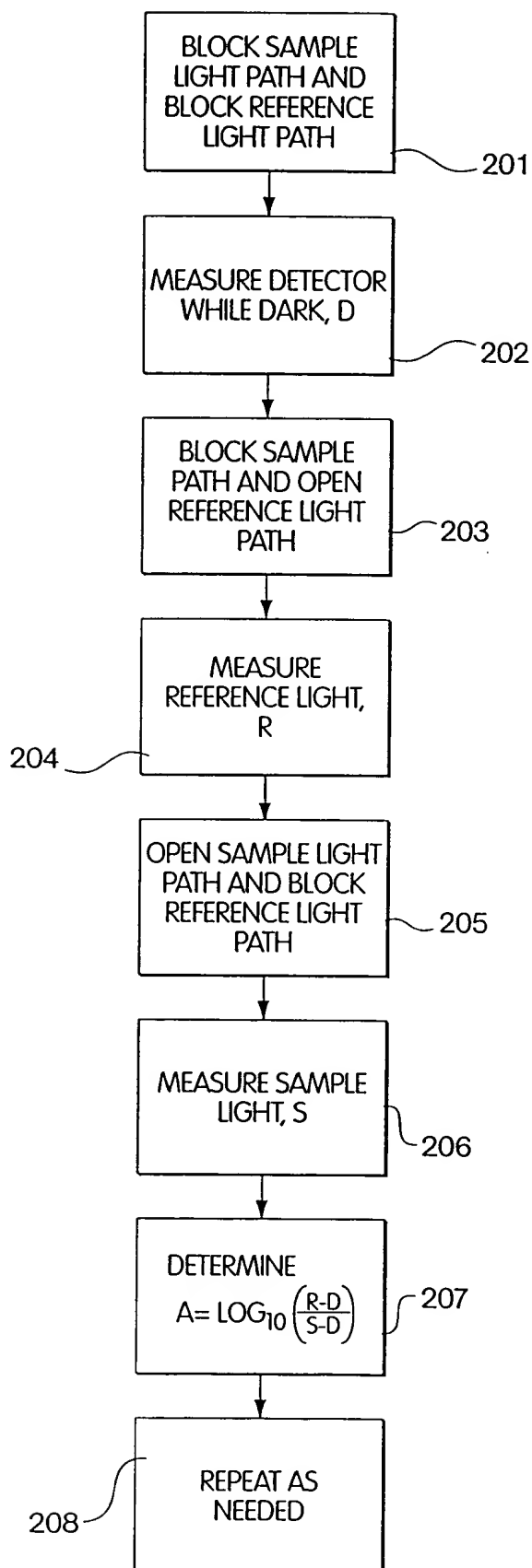
[illegible]

Fig. 2

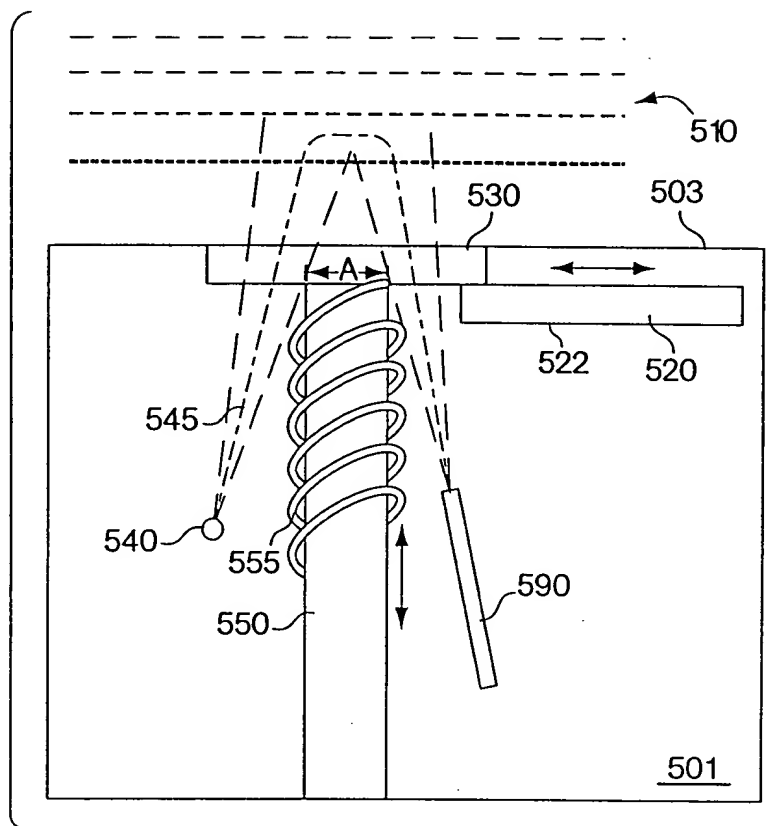


Fig. 3A

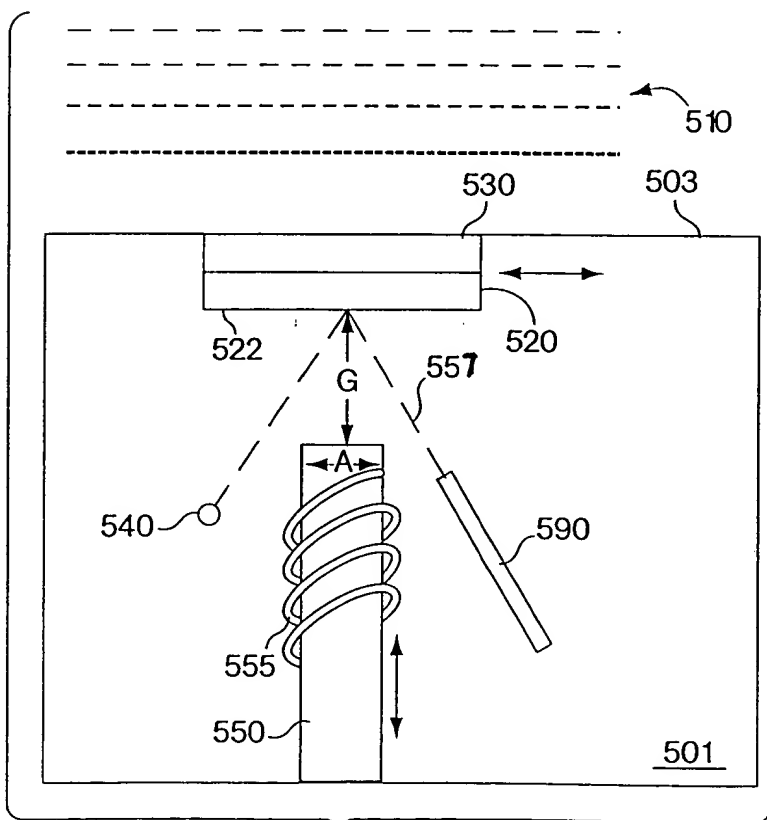
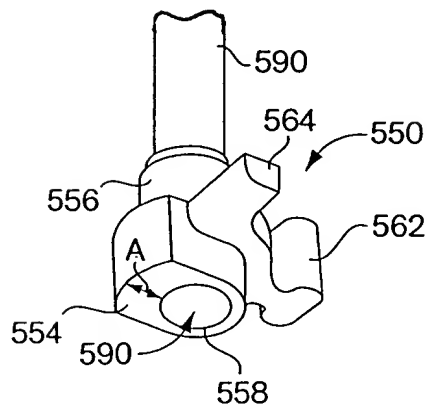
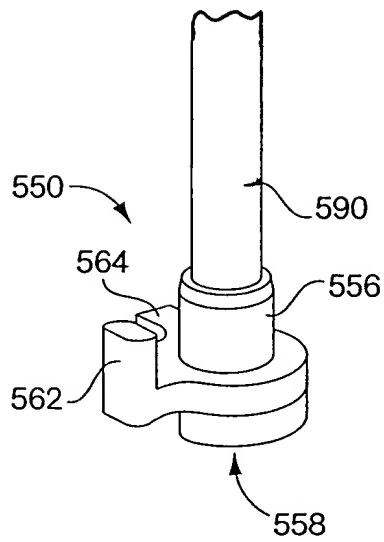


Fig. 3B



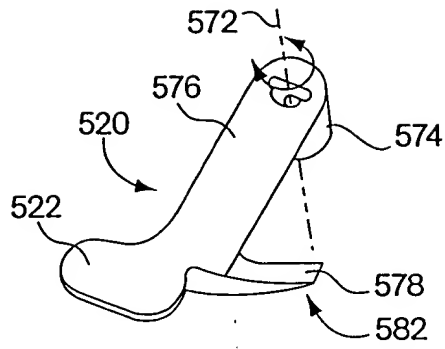


Fig. 5 A

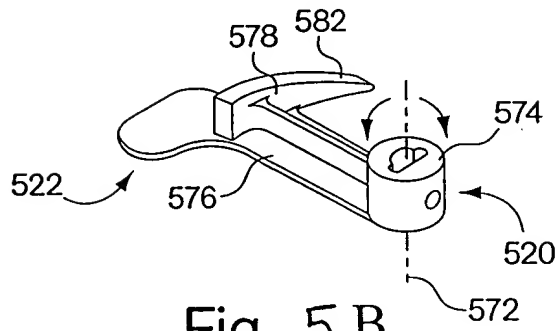


Fig. 5 B

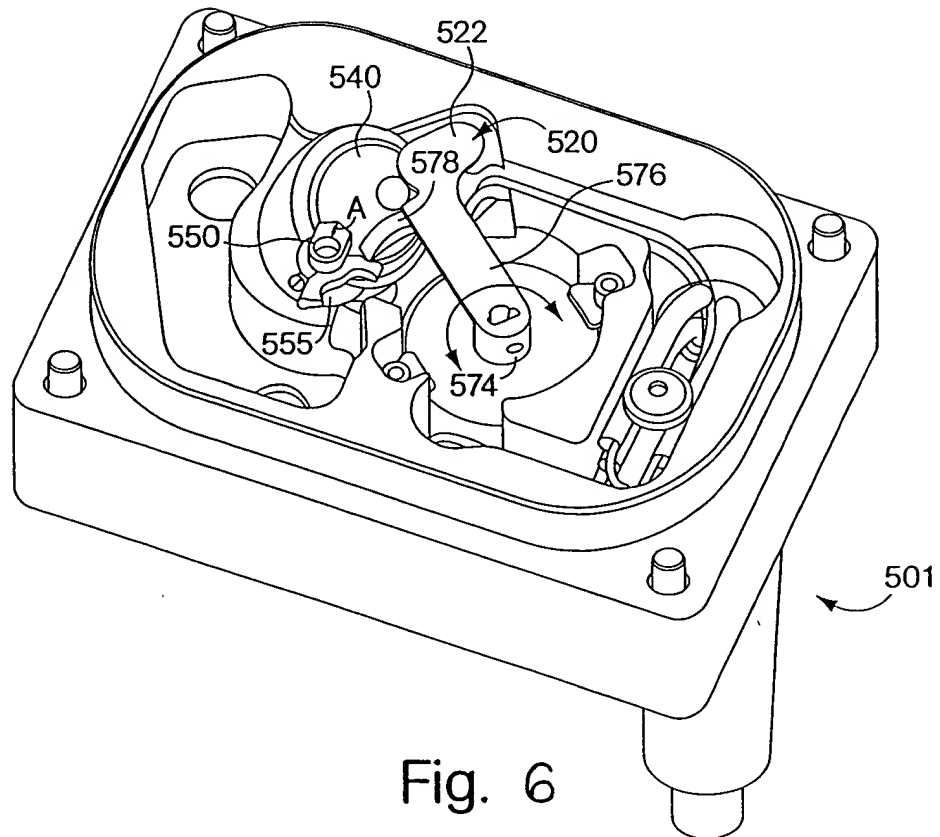


Fig. 6